

Conventional Load Balancer (Prior Art)

FIG. 1

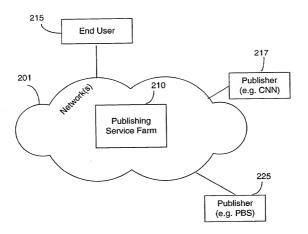


FIG. 2

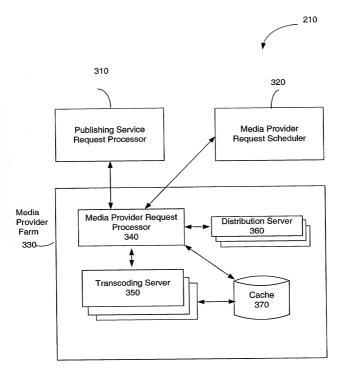
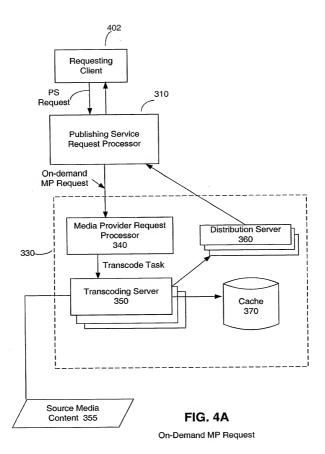


FIG. 3



0104-23.vsd/4

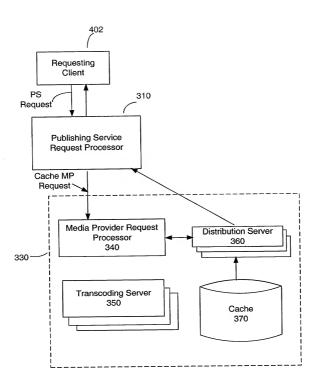


FIG. 4B
Cached MP Request

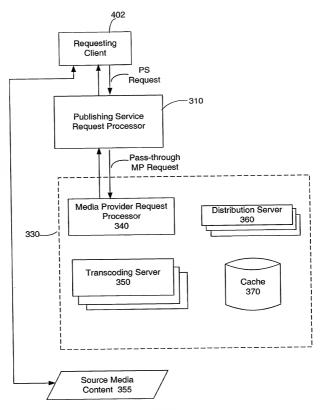


FIG. 4C
Pass-Through MP Request

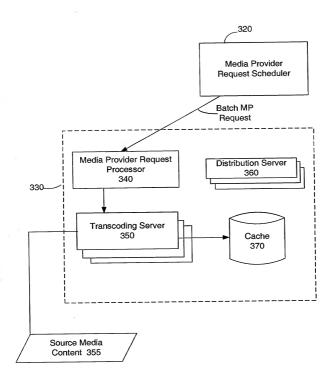


FIG. 4D

Batch Transcoding MP Request

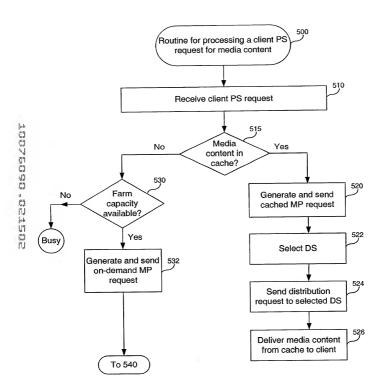


FIG. 5A

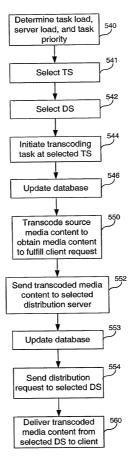


FIG. 5B

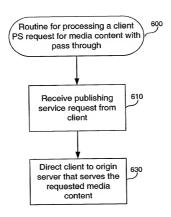


FIG. 6

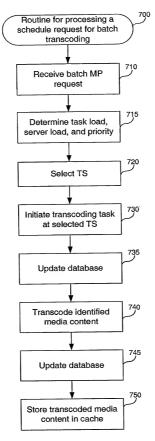


FIG. 7

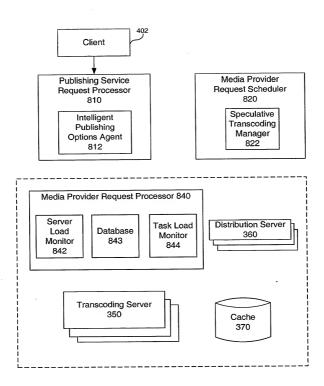


FIG. 8

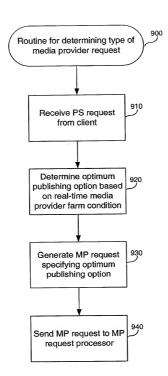


FIG. 9

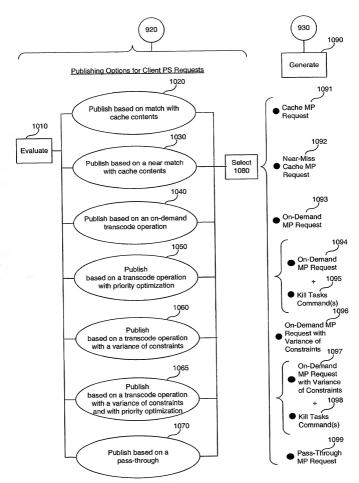


FIG. 10

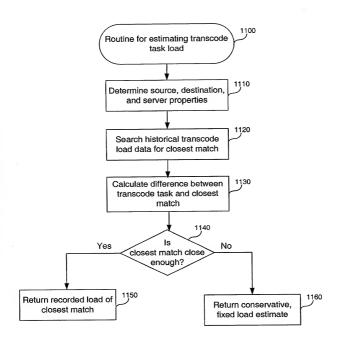


FIG. 11

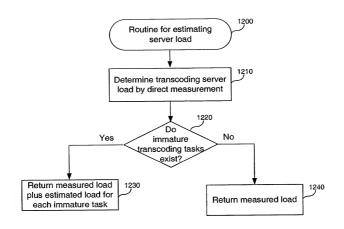


FIG. 12

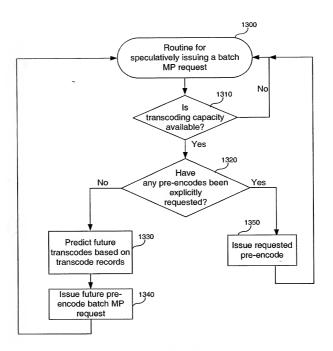


FIG. 13

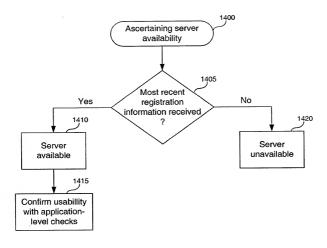


FIG. 14

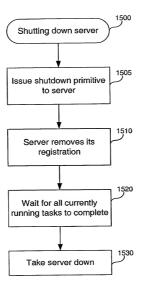


FIG. 15